

PATENT

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ABSTRACT

A method and apparatus for scaling display for displaying an input signal are disclosed, the method and apparatus particularly being useful for displaying return-to-zero signals. The technique includes a first step of sampling the signal. The sampled signal is searched for a zero space pattern. Then, a first zero space is located and a second zero space, following the first zero space, is located. A bit period of the input signal is calculated. Finally, the input signal is displayed using the calculated bit period as the basis for a scale.

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